

## **News Release**

31st October 2017

The patent infringement lawsuit filed by SEMICAPS against Hamamatsu in California has been held in abeyance pending an Inter Partes Review (IPR) filed by Hamamatsu against US Patent 7,623,982. The IPR was filed with the USPTO in September 2017.

SEMICAPS will rigorously defend the IPR action and the company is confident that the relevant claims of the patent that are involved in the patent infringement lawsuit will survive the IPR.

For more details, visit

[https://www.docketalarm.com/cases/PTAB/IPR201702110/Inter\\_Partес\\_Review\\_of\\_U.S.\\_Pat.\\_7623982/](https://www.docketalarm.com/cases/PTAB/IPR201702110/Inter_Partес_Review_of_U.S._Pat._7623982/)

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## **News Release**

27th June 2017

SEMICAPS Pte Ltd, a wholly owned subsidiary of SEMICAPS Corporation Pte Ltd has filed a patent infringement lawsuit against Hamamatsu Corporation, Photonics Management Corp and Hamamatsu Photonics KK. The court complaint was submitted on 14th June 2017 in the Northern District of California, USA.

SEMICAPS alleges that Hamamatsu's Phemos and iPhemos systems fitted with the M10383 Digital Lock-In Kit or any other similar kit infringes US Patent No. 7,623,982. This digital lock-in technique (together with laser pulsing) improves the sensitivity of OBIRCH and TIVA analysis significantly. Hamamatsu's Lock-In Kit and the use of such a Kit in the Phemos and iPhemos systems allegedly violate SEMICAPS' intellectual property rights.

Licensing negotiation was not fruitful and SEMICAPS was left with no other viable alternative except to seek redress through the law courts. SEMICAPS is represented by David D. Schumann of Marton Ribera Schumann & Chang LLP in San Francisco.

Please see also: <http://norcalrecord.com/stories/511130914-semicaps-pte-ltd-alleges-hamamatsu-kit-infringes-patent>

## **About SEMICAPS**

SEMICAPS is the world's most technologically advanced company in the field of failure localization analysis in semiconductors. Used by leading companies like GlobalFoundries, AMD and Intel for their design-debug and yield analyses, the SEMICAPS laser probe microscope is capable for semiconductor technology nodes as small as 10 nm. Further, besides the standard laboratory configurations, the SEMICAPS Wafer Probing platform, the SEMICAPS 5000, allows semiconductor wafer of complex logic chips to be analysed without the need for sorting, dicing and packaging. This saves weeks for foundries undergoing their yield enhancement cycle.

SEMICAPS, founded in 1988, is a technology spinoff from the National University of Singapore.